11. Record the output's value (0 or 1) in the first row of the NOT gate truth table in Figure 10.

А	Actual Output
0	
1	Figure 10

8. Record the output's value (0 or 1) in the first row of the AND gate truth table in Figure 14.

Α	В	Actual Output
0	0	
0	1	
1	0 Figu	re 14
1	1	

8. Record the output's value (0 or 1) in Figure 20.

the first row of the OR gate truth table in

Α	В	Actual Output
0	0	
0	1	
1	0 Figu	e 20
1	1	

9. Continue testing the other three recording the output values in the Figure 20.

combinations of inputs and outputs and corresponding row of the truth table in

- 10. Verify that the outputs you obtained are the same as what you expected.
- 11. Disconnect power from the solderless breadboard.
- 12. Carefully remove the 74LS32.